

FIG. 1A

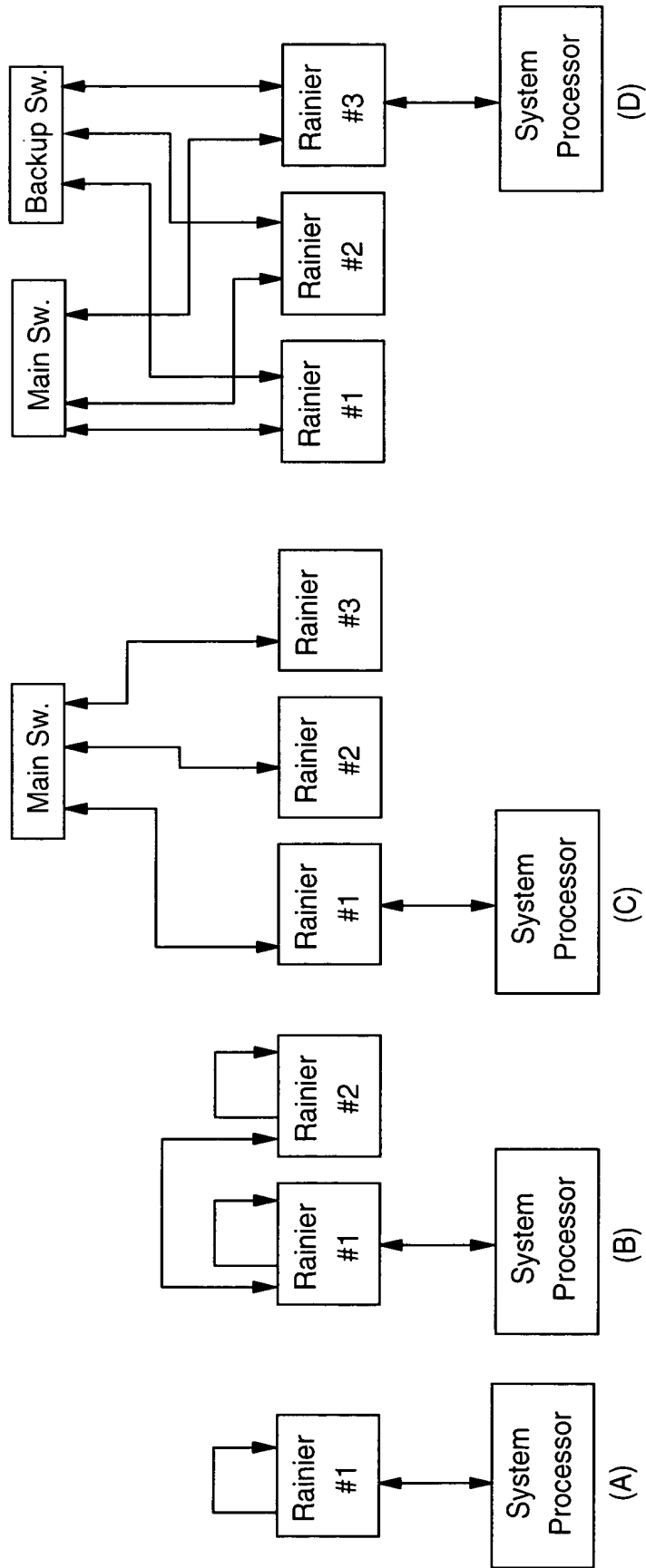
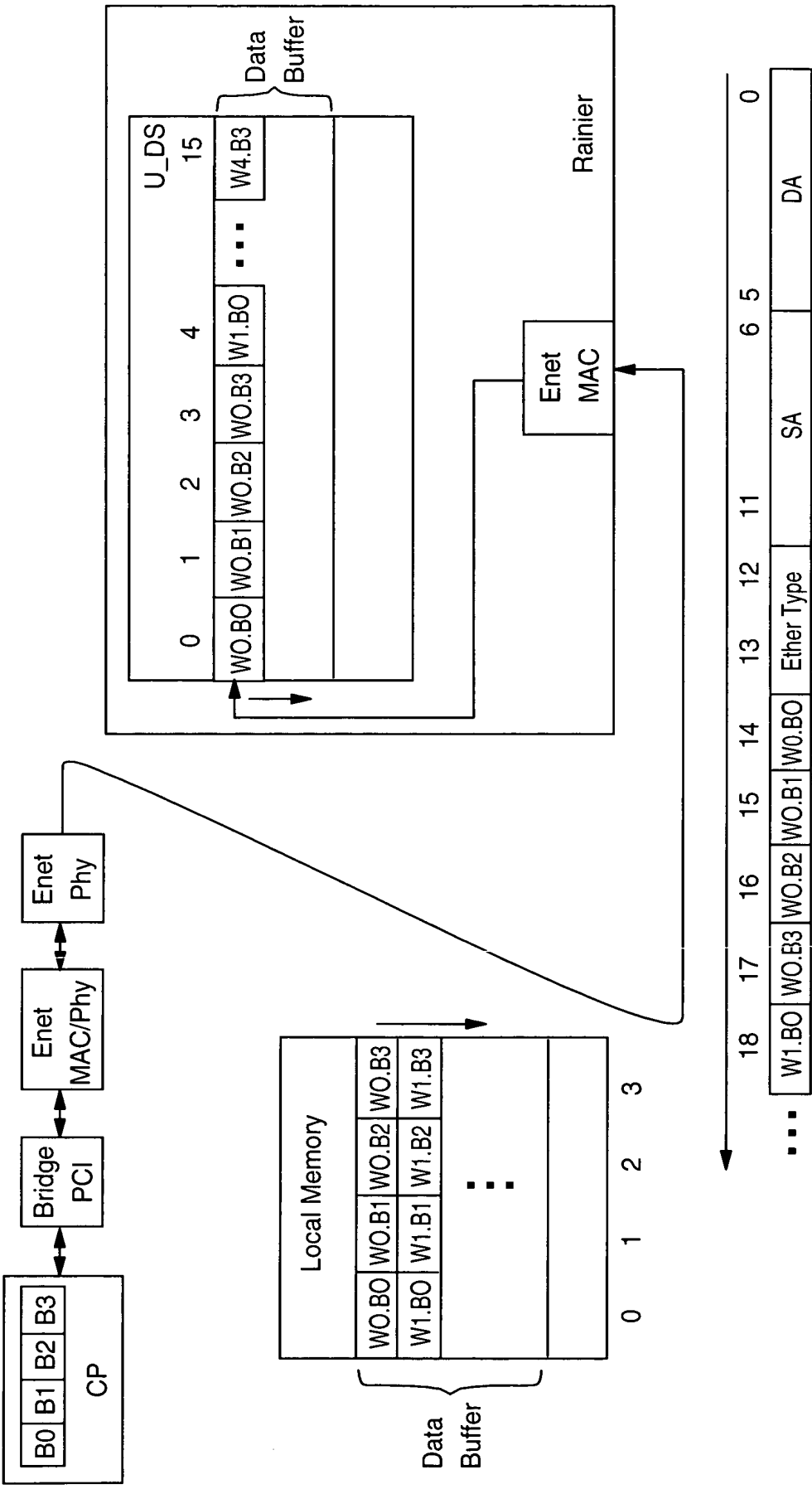


FIG. 2 System Configurations



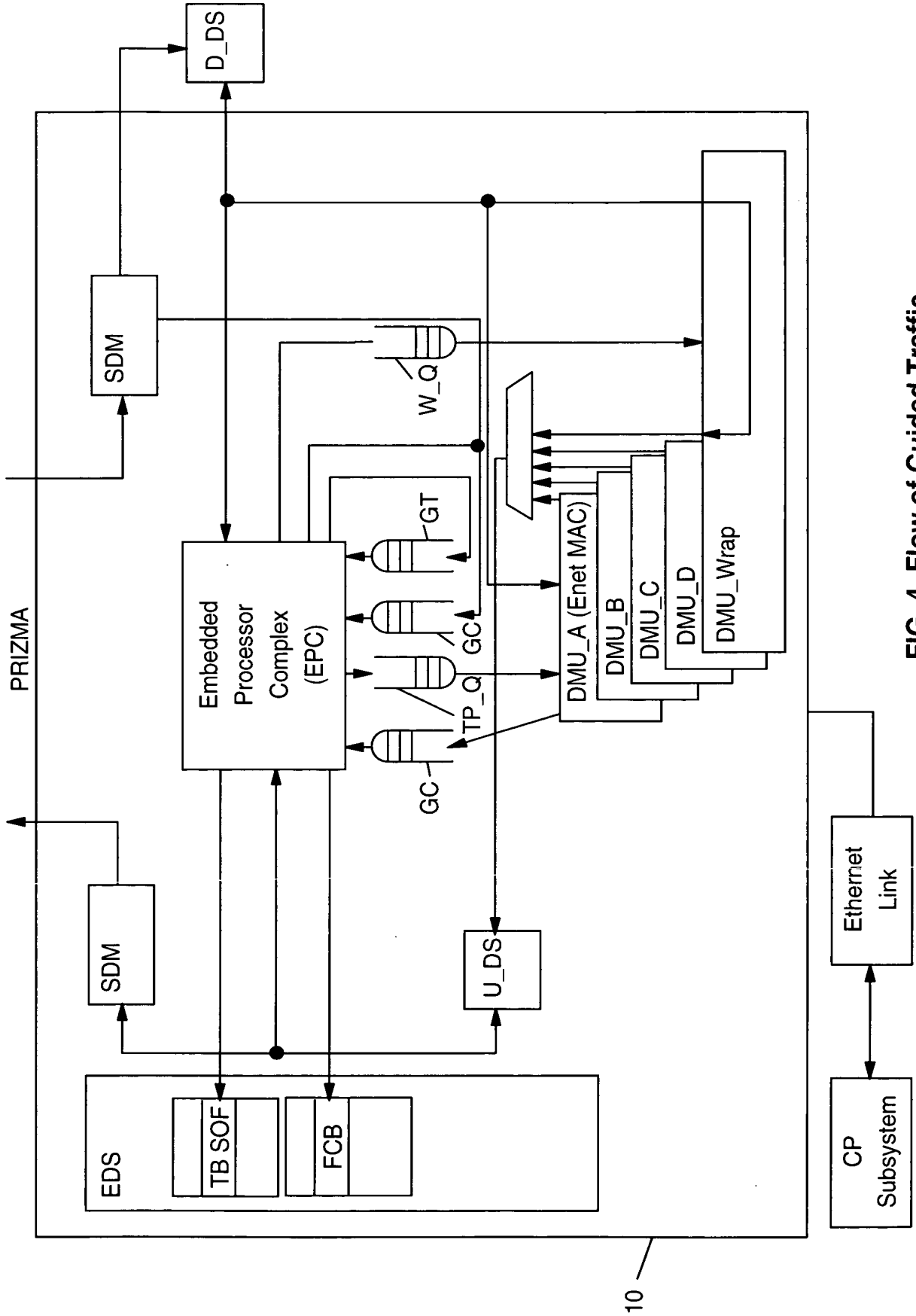


FIG. 4 Flow of Guided Traffic

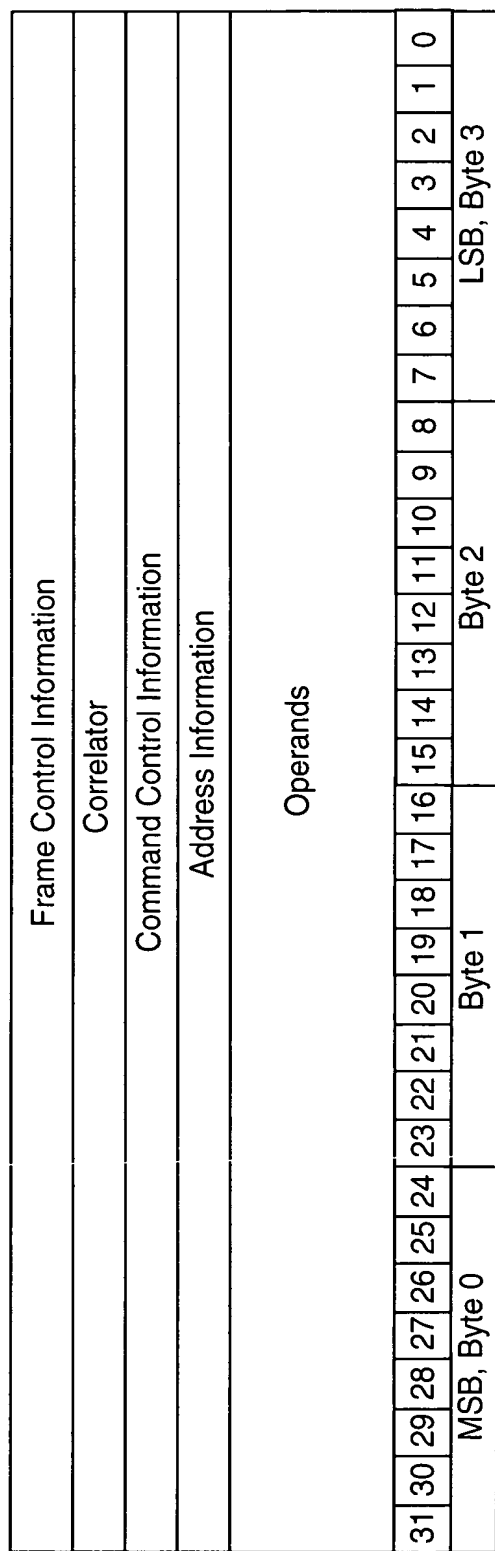


FIG. 5

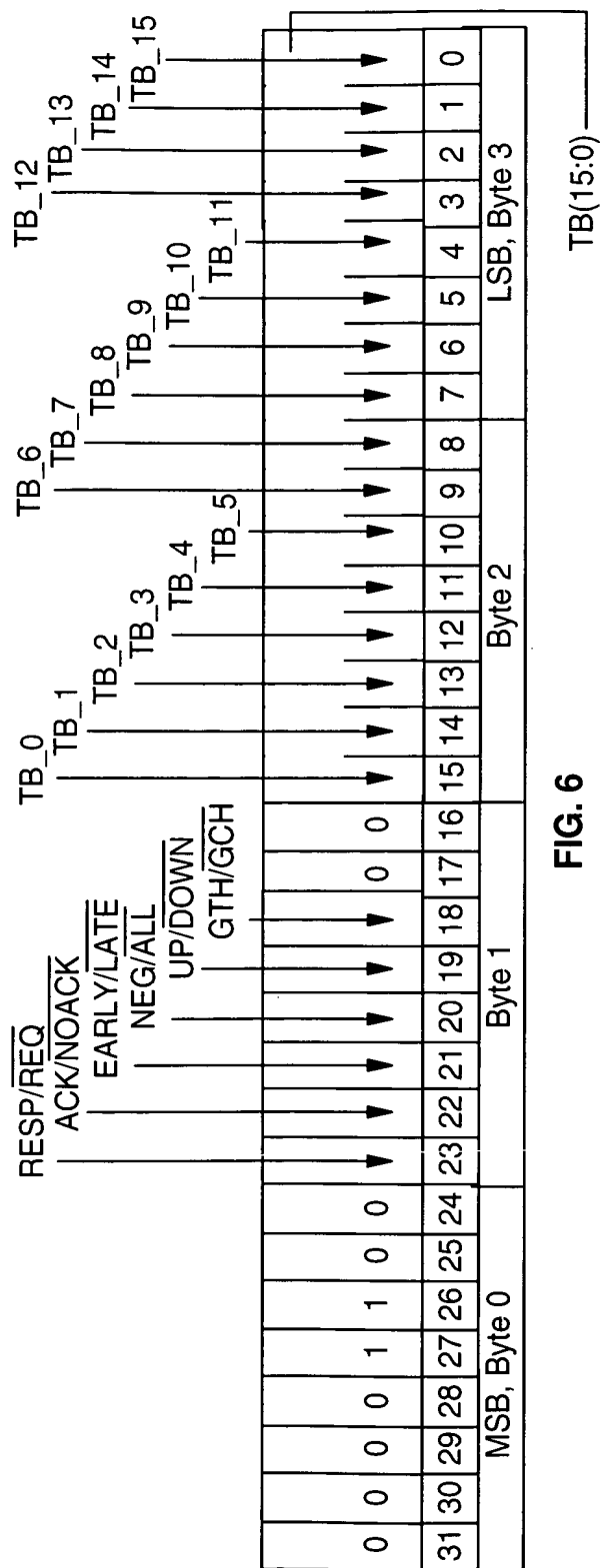


FIG. 6

Word 1																															
Correlator																															
31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
MSB, Byte 0								Byte 1								Byte 2								LSB, Byte 3							

FIG. 7 Correlator

L																Completion Code								res				GC Type			
MSB, Byte 0																Byte 1								Byte 2				LSB, Byte 3			
31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
MSB, Byte 0																Byte 1								Byte 2				LSB, Byte 3			

Word 2 Cmd Word 0

FIG. 8 Command Control Information Format

Word 3 Cmd Word 1																															
Address																															
31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
MSB, Byte 0								Byte 1								Byte 2								LSB, Byte 3							

Word 3 Cmd Word 1

FIG. 9 Addressing Information Format

Island ID	Structure Address	Element Address	Word Addr
5	23		4
32			

FIG. 10 General Form of Structure Addressing

*These islands are not accessible via the Web interface

FIG. 11 Addressing, Island Encoding

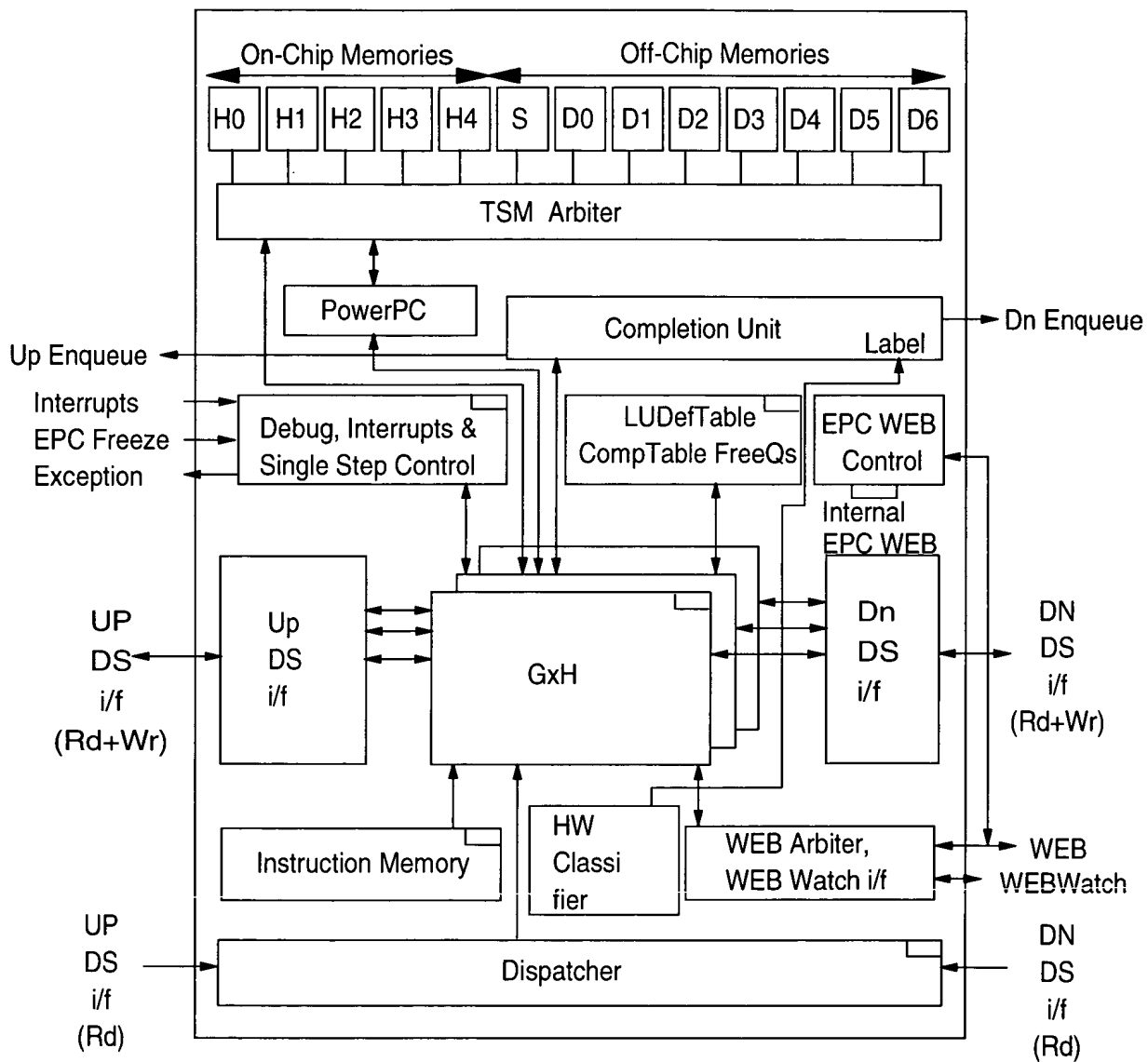


FIG. 12A BLOCK DIAGRAM OF THE EPC



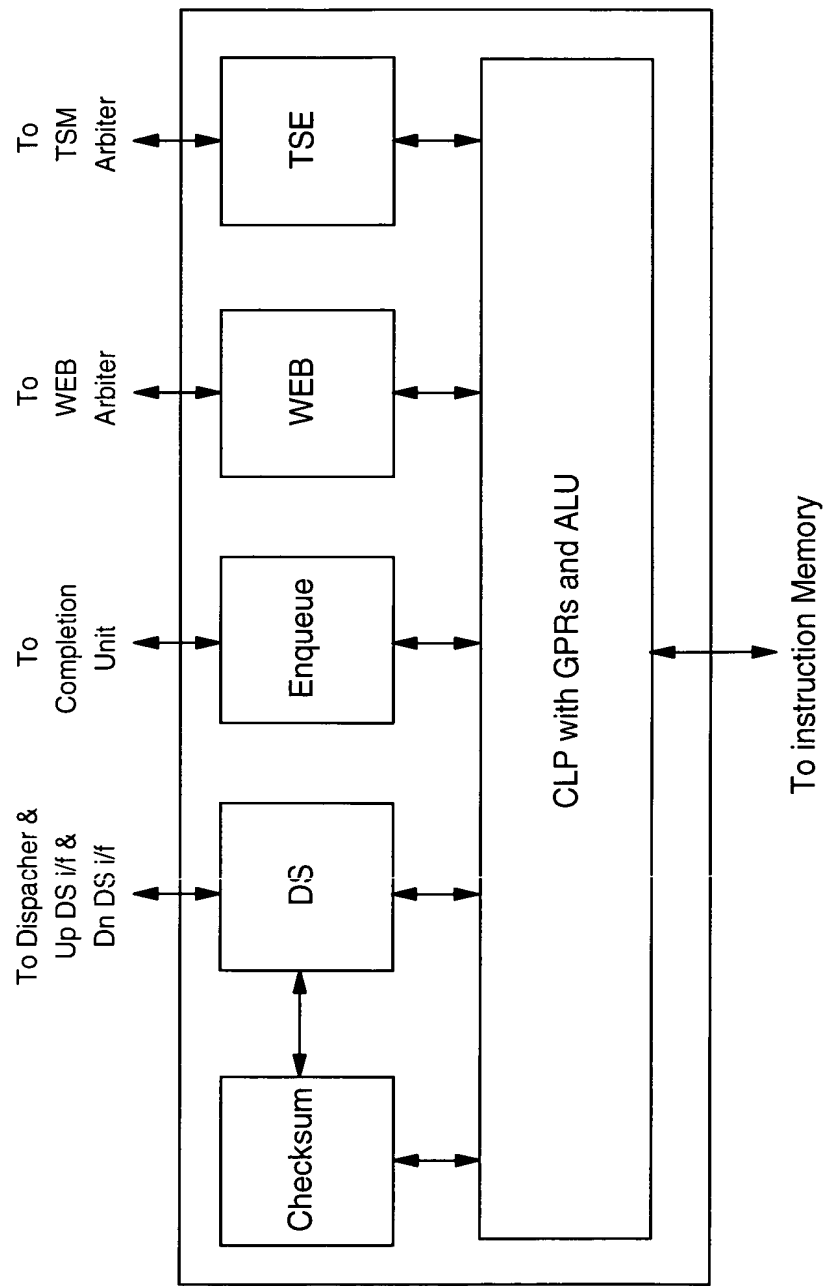


FIG. 12C

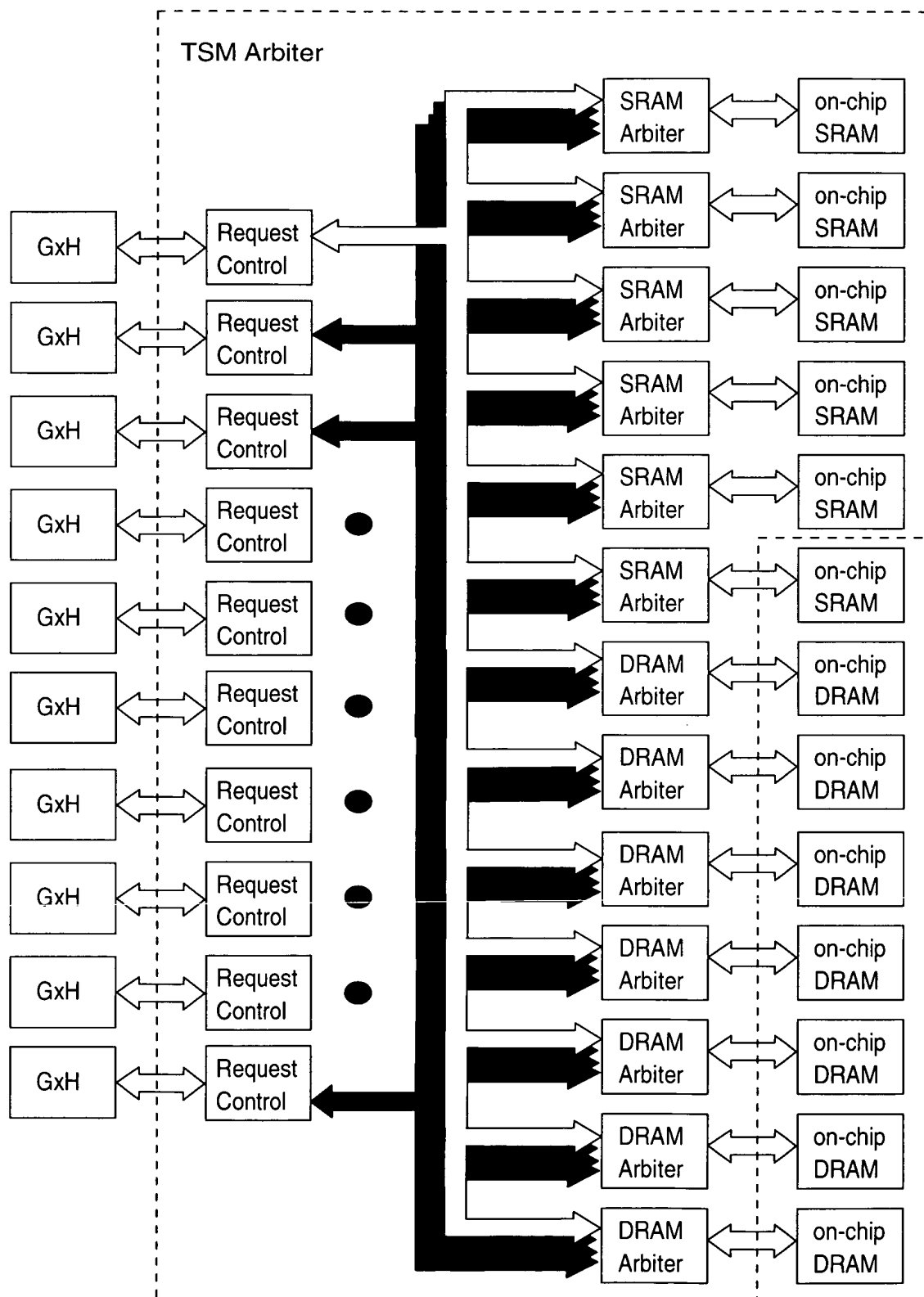


FIG. 13 (Memory Complex)

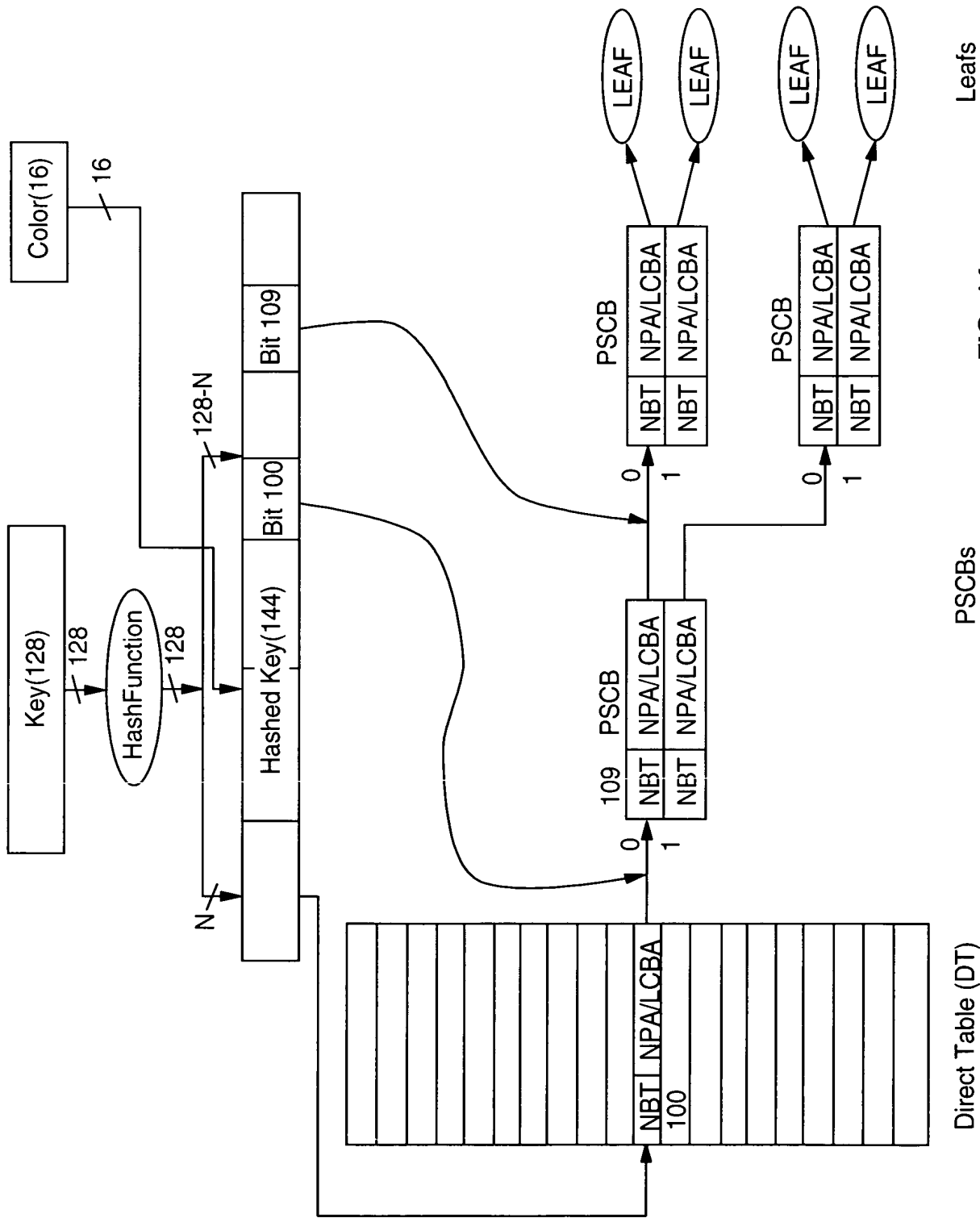
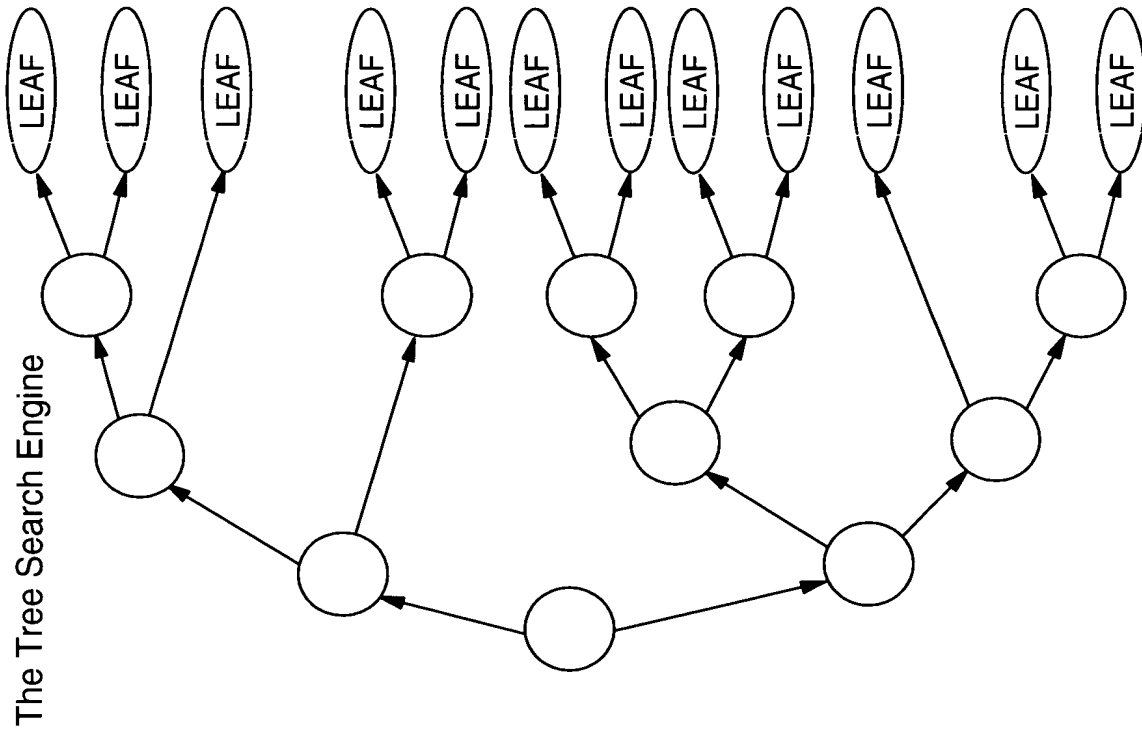
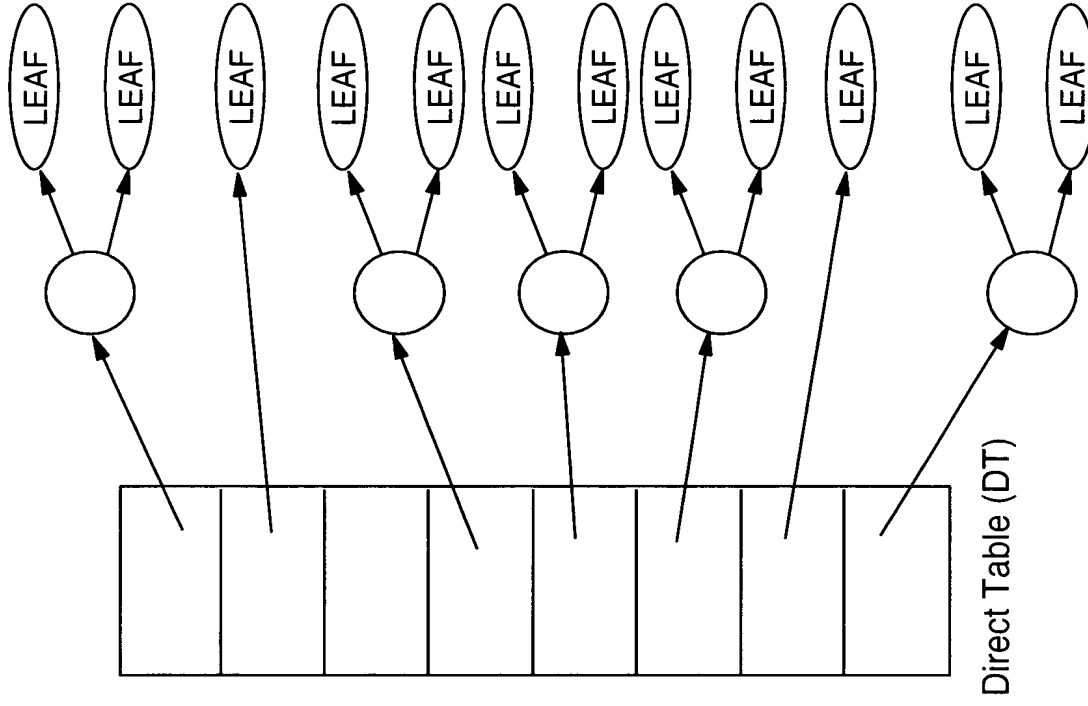


FIG. 14



Datastructure without using a Direct Table



Datastructure with using a Direct Table

FIG. 15

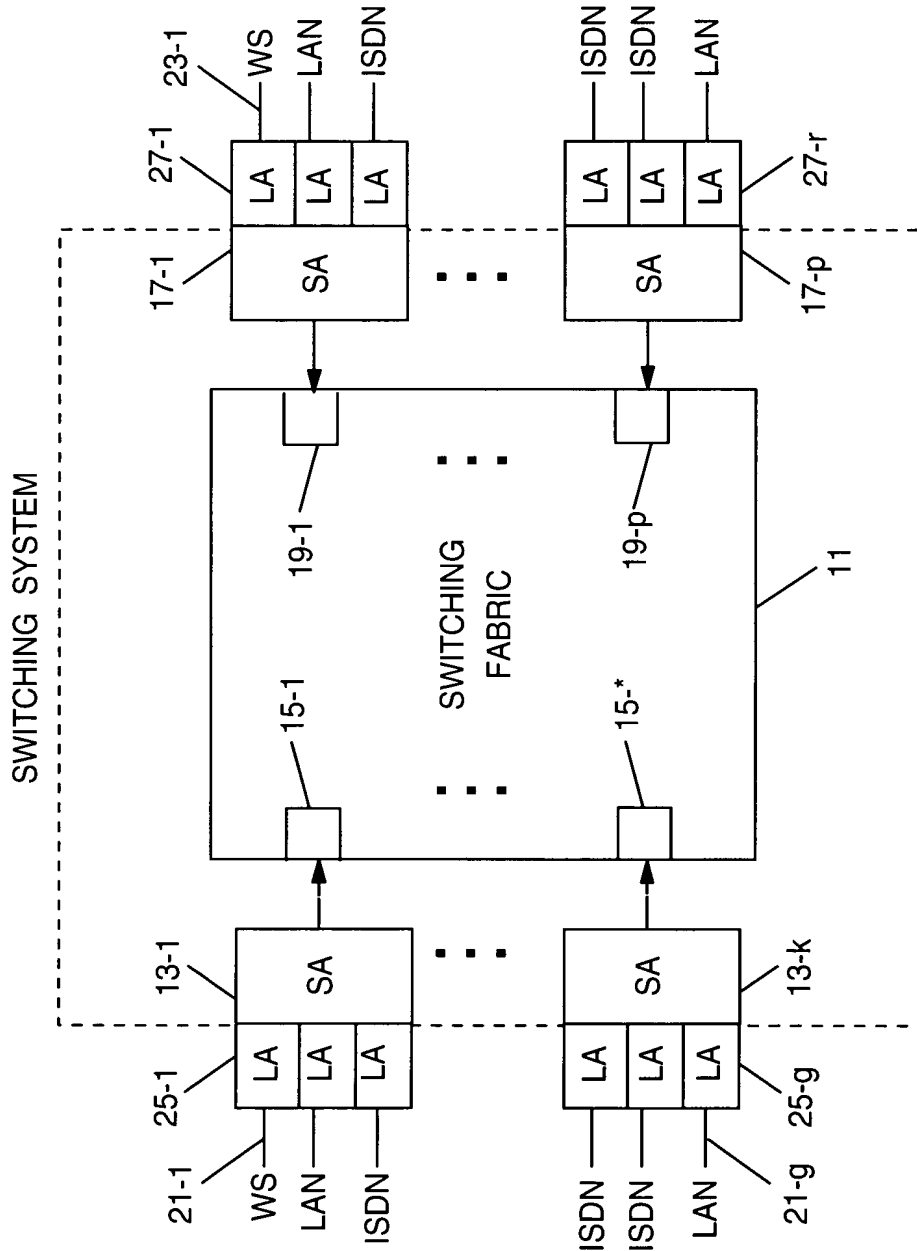


FIG. 16

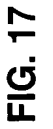


FIG. 17

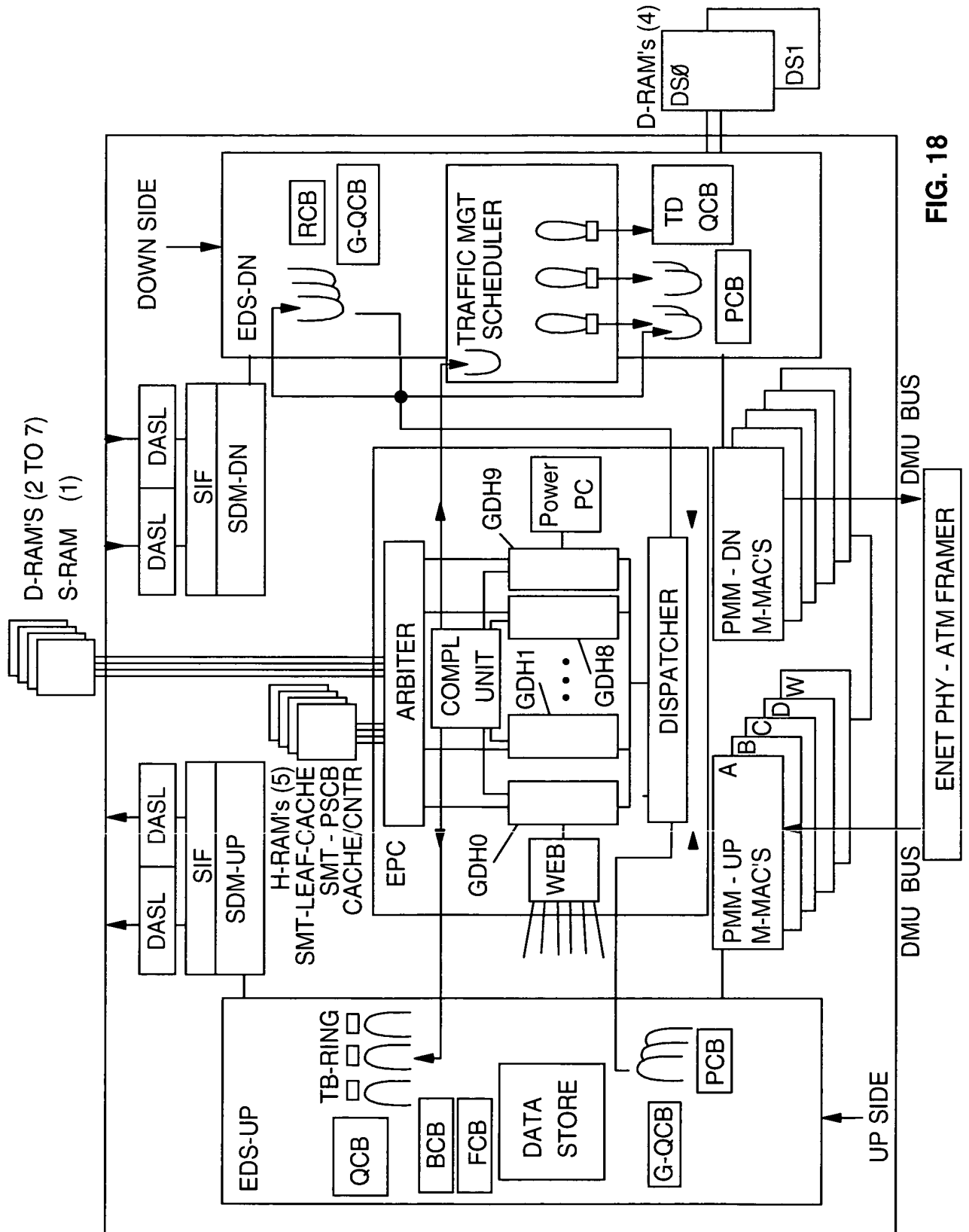


FIG. 18